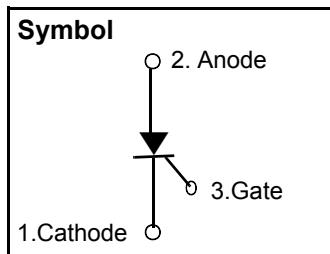
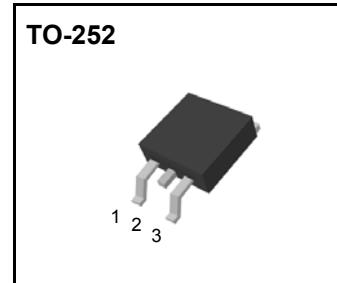


## Sensitive Gate Silicon Controlled Rectifiers

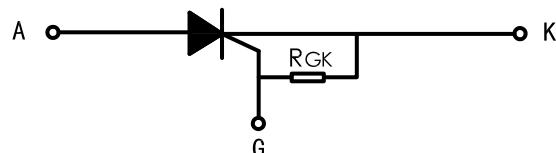


$BV_{DRM} = 600V$
$I_{T(RMS)} = 8 A$
$I_{TSM} = 70A$



### Features

- ◆ Repetitive Peak Off-State Voltage : 600V
- ◆ R.M.S On-State Current (  $I_{T(RMS)}= 8 A$  )



### General Description

Apollo Electron's SCR is suitable for the application where requiring high bidirectional blocking voltage capability and also suitable for over voltage protection, motor control circuit in power tool, inrush current limit circuit and heating control system.

### ABSOLUTE MAXIMUM RATINGS

Paramter	Symbol	Value	Units
Storage Junction Temperature Range	T <sub>stg</sub>	-40~150	°C
Operating Junction Temperature Range	T <sub>j</sub>	-40~110	°C
Repetitive Peak Off-State Voltage T <sub>j</sub> =25°C	V <sub>DRM</sub>	600	V
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	600	V
RMS On-State Current (180° conduction angle) T <sub>I</sub> =105°C	I <sub>T(RMS)</sub>	8	A
Average On-Stage Current (180° conduction angle) T <sub>I</sub> =105°C	I <sub>T(AV)</sub>	5	A
Non Repetitive Surge Peak On-State Current(T <sub>j</sub> =25°C)	I <sub>TSM</sub>	70	A
tp=8.3ms		73	
I <sup>2</sup> t Value For Fusing tp=10ms	I <sup>2</sup> t	24.5	A <sup>2</sup> s
Critical Rate Of Rise Of On-State Current I <sub>G</sub> =2 x I <sub>GT</sub> , tr≤100ns, f=50Hz, T <sub>j</sub> =110°C	dI/dt	50	A/us
Peak Gate Current tp=20us, T <sub>j</sub> =125°C	I <sub>GM</sub>	4	A
Average Gate Power Dissipation T <sub>j</sub> =125°C	P <sub>G(AV)</sub>	1	W



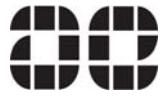
# CD8C60SR

ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)

Symbol	Test Condition			Unit
I <sub>GT</sub>	V <sub>D</sub> =6V R <sub>L</sub> =140Ω	MAX	100	uA
V <sub>GT</sub>		MAX	0.8	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> R <sub>L</sub> =3.3KΩ R <sub>GK</sub> =220Ω T j=125°C	MIN	0.1	V
I <sub>L</sub>	I <sub>G</sub> =1mA R <sub>GK</sub> =1KΩ	MAX	6	mA
I <sub>H</sub>	I <sub>T</sub> =50mA R <sub>GK</sub> =1KΩ	MAX	5	mA
V <sub>TM</sub>	I <sub>T</sub> =16A t <sub>p</sub> =380μS T j=25°C	MAX	1.6	V
dV/dt	V <sub>D</sub> =65% V <sub>DRM</sub> R <sub>GK</sub> =220Ω	MIN	5	V/μs
I <sub>DRM</sub>	V <sub>DRM</sub> =V <sub>RRM</sub> R <sub>GK</sub> =220Ω T J=25°C	MAX	5	uA
I <sub>RRM</sub>	V <sub>DRM</sub> =V <sub>RRM</sub> R <sub>GK</sub> =220Ω T J=125°C		1	mA
R <sub>GK</sub>			6~35	KΩ

## THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R <sub>th(J-c)</sub>	Junction To Case(DC)	20	°C/W



# CD8C60SR

Fig 1 Maximum Average Power Dissipation vs. Average On-State Current

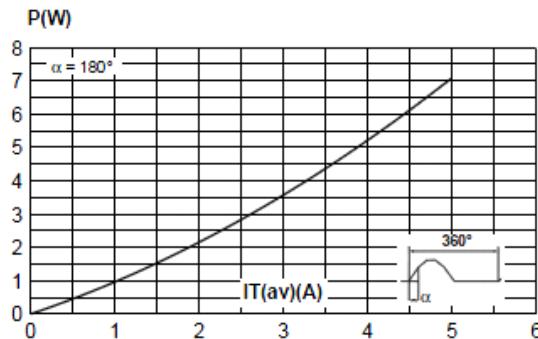


Fig.3 Surge Peak On-State Current vs. Number Of Cycles

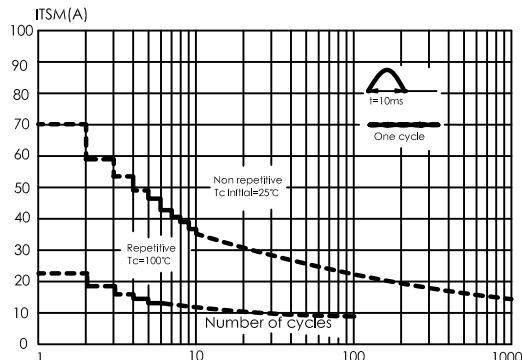


Fig.5 Relative Variation Of Gate Trigger Current, Holding Current and Latching Current vs. Junction Temperature (typical values)

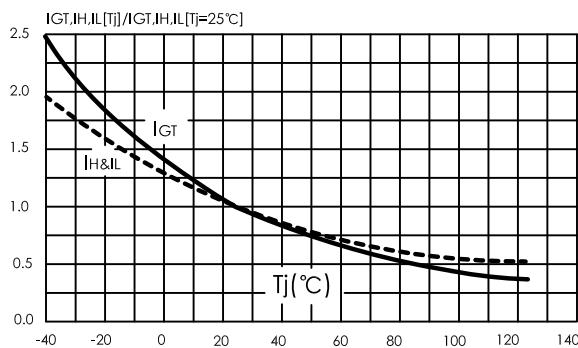


Fig 2 Average And D.C. On-State Current vs. Lead Temperature

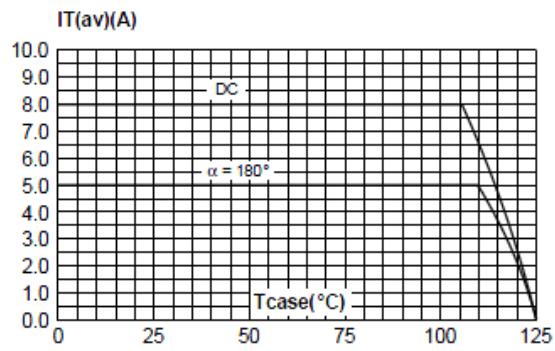


Fig.4 Non-Repetitive Surge Peak On-State Current For a Sinusoidal Pulse With Width  $t_p < 10\text{ms}$ , And Corresponding Value Of  $I^2t$

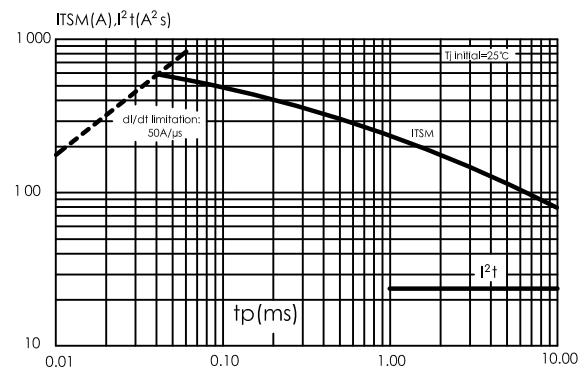
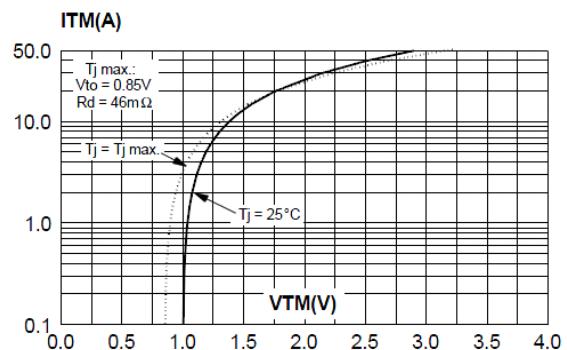


Fig.6 On-State Characteristics (maximum values)



**CD8C60SR****TO-252 Package Dimension**

Symbol	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20	2.3	2.40	0.087	0.0905	0.094
B	1.30	1.4	1.50	0.051	0.055	0.059
b	0.55	0.6	0.65	0.022	0.024	0.026
b1	0.46	0.51	0.56	0.018	0.02	0.022
C	0.46	0.51	0.56	0.018	0.02	0.022
D	6.40	6.5	6.60	0.252	0.256	0.260
D1	5.20	5.3	5.40	0.205	0.2085	0.212
E	5.40	2.285	5.60	0.212	0.09	0.220
e1	2.25	2.3	2.35	0.089	0.091	0.093
e2	4.50	4.6	4.70	0.177	0.181	0.185
L1	9.25	9.5	9.75	0.346	0.365	0.384
L2	0.95	1.2	1.45	0.037	0.047	0.057

